	Document ID	Issue Date	Pages	Title	Inventor
1	US 20070101173 A1	20070503	73	Apparatus, architecture, and method for integrated modular server system providing dynamically power-managed and work-load managed network devices	Fung; Henry T.
2	US 20060265609 A1	20061123	79	System, method, and architecture for dynamic service power management and dynamic workload management for multi-server environment	Fung; Henry T.
3	US 20060265608 A1	20061123	78	System, method, and architecture for dynamic server power management and dynamic workload management for multi-server environment	Fung; Henry T.
4	US 20060259797 A1	20061116	74	System, method, architecture, and computer program product for dynamic power management in a computer system	Fung; Henry T.
5	US 20060259796 A1	20061116	42	System, method, and architecture for dynamic server power management and dynamic workload management for multi-server environment	Fung; Henry T.
6	US 20060253717 A1	20061109	77	System and method for activity or event based dynamic energy conserving server reconfiguration	Fung; Henry T.
7	US 20060248361 A1	20061102	74	Dynamic power and workload management for multi-server system	Fung; Henry T.
8	US 20060248360 A1	20061102	74	Multi-server and multi-CPU power management system and method	Fung; Henry T.
9	US 20060248359 A1	20061102	75	Power on demand and workload management system and method	Fung; Henry T.
10	US 20060248358 A1	20061102	75	System, method, architecture, and computer program product for dynamic power management in a computer system	Fung; Henry T.
11	US 20060248325 A1	20061102	76	Apparatus and method for modular dynamically power managed power supply and cooling system for computer systems, server applications, and other electronic devices	Fung; Henry T.

	Document ID	Issue Date	Pages	Title	Inventor
12	US 20060248324 A1	20061102	73	Apparatus, architecture, and method for integrated modular server system providing dynamically power-managed and work-load managed network devices	Fung; Henry T.
13	US 20050283582 A1	20051222	14	Smart memory read out for power saving	Aakjer, Thomas
14	US 20050223258 A1	20051006	15	Apparatus employing real-time power conservation and thermal management	Watts, La Vaughn F. JR.
15	US 20050223257 A1	20051006	15	Processor employing implementing real-time power conservation and thermal management	Watts, La Vaughn F. JR.
16	US 20050223256 A1	20051006	15	Method for implementing thermal and power management in a processor and/or apparatus and/or system employing the same	Watts, La Vaughn F. JR.
17	US 20050223255 A1	20051006	15	Method for implementing thermal and power management in a processor and/or apparatus and/or system employing the same	Watts, La Vaughn F. JR.
18	US 20050223254 A1	20051006	15	Method for implementing thermal and power management in a processor and/or apparatus and/or system employing the same	Watts, La Vaughn F. JR.
19	US 20050204179 A1	20050915	14	Method for controlling power consumption associated with a processor	Watts, LaVauchn F. JR. et al.
20	US 20050204178 A1	20050915	15	Method for controlling power consumption associated with a processor	Watts, LaVaughn F. JR. et al.
21	US 20050204177 A1	20050915	14	Method for providing real-time power conservation in a processor	Watts, Lavaughn F. JR. et al.
22	US 20050198543 A1	20050908	14	Processor having real-time power conservation	Watts, LaVaughn F. JR. et al.
23	US 20050174678 A1	20050811	13	Stepping power consumption levels in a hard disk drive to maximize performance while minimizing power consumption	Zayas, Fernando A. et al.

	Document ID	Issue Date	Pages	Title	Inventor
24	US 20050144491 A1	20050630	15	Variable power consumption levels in a hard disk drive	Zayas, Fernando A.
25	US 20050108582 A1	20050519	74	System, architecture, and method for logical server and other network devices in a dynamically configurable multi-server network environment	Fung, Henry T.
26	US 20040225908 A1	20041111	15	Processor having real-time power conservation	Watts, LaVaughn F. JR. et al.
27	US 20040225906 A1	20041111	16	Real-time power conservation for portable computers	Watts, LaVaughn F. et al.
28	US 20040203834 A1	20041014	112	Remote radio data communication system with data rate switching	Mahany, Ronald L.
29	US 20040166943 A1	20040826	54	External memory system having programmable graphics processor for use in a video game system of the like	San, Jeremy E. et al.
30	US 20040073933 A1	20040415	59	Network supporting roaming, sleeping terminals	Gollnick, Charles D. et al.
31	US 20040023651 A1	20040205	59	Network supporting roaming, sleeping terminals	Gollnick, Charles D. et al.
32	US 20040015079 A1	20040122	109	Ultrasound probe with integrated electronics	Berger, Noah et al.
33	US 20030200473 A1	20031023	77	System and method for activity or event based dynamic energy conserving server reconfiguration	Fung, Henry T.
34	US 20030196126 A1	20031016	48	System, method, and architecture for dynamic server power management and dynamic workload management for multi-server environment	Fung, Henry T.
35	US 20030188208 A1	20031002	79	System, method, and architecture for dynamic server power management and dynamic workload management for multi-server environment	Fung, Henry T.

	Document ID	Issue Date	Pages	Title	Inventor
36	US 20030131273 A1	20030710	16	PROCESSOR HAVING REAL-TIME POWER CONSERVATION	Watts, LaVaughn F. JR. et al.
37	US 20030131272 A1	20030710	16	Processor having real-time power conservation	Watts, LaVaughn F. JR. et al.
38	US 20030078006 A1	20030424	276	Remote radio data communication system with data rate switching	Mahany, Ronald L.
39	US 20030073894 A1	20030417	55	Ultrasound probe with integrated electronics	Chiang, Alice M. et al.
40	US 20020126737 A1	20020912	10	Background processing and searching for a communication channel	Ofir, Edlis et al.
41	US 20020104033 A1	20020801	16	Processor having real-time power conservation	Watts, LaVaughn F. JR. et al.
42	US 20020099514 A1	20020725	30	Processor having real-time power conservation and thermal management	Watts, La Vaughn F. JR.
43	US 20020084863 A1	20020704	7	System and method for reduceing startup time in a temperature controlled crystal oscillator	Sutliff, Richard N. et al.
44	US 20020062454 A1	20020523	73	Dynamic power and workload management for multi-server system	Fung, Henry T.
45	US 20020050999 A1	20020502	56	External memory system having programmable graphics processor for use in a video game system or the like	San, Jeremy E. et al.
46	US 20020007464 A1	20020117	75	Apparatus and method for modular dynamically power managed power supply and cooling system for computer systems, server applications, and other electronic devices	Fung, Henry T.
47	US 20020004915 A1	20020110	75	System, method, architecture, and computer program product for dynamic power management in a computer system	Fung, Henry T.

	Document ID	Issue Date	Pages	Title	Inventor
48	US 20020004913 A1	20020110	74	Apparatus, architecture, and method for integrated modular server system providing dynamically power-managed and work-load managed network devices	Fung, Henry T.
49	US 20020004912 A1	20020110	74	System, architecture, and method for logical server and other network devices in a dynamically configurable multi-server network environment	Fung, Henry T.
50	US 20010053174 A1	20011220	82	Spread spectrum localizers	Fleming, Robert Alan et al.
51	US 20010043224 A1	20011122	56	External memory system having programmable graphics processor for use in a video game system or the like	San, Jeremy E. et al.
52	US 20010040577 A1	20011115	56	External memory system having programmable graphics processor for use in a video game system or the like	San, Jeremy E. et al.
53	US 20010033607 A1	20011025	82	Spread spectrum localizers	Fleming, Robert Alan et al.
54	US 20010014922 A1	20010816	67	Interface circuit device for performing data sampling at optimum strobe timing	Kuge, Shigehiro
55	US 20010005892 A1	20010628	16	Real-time power conservation for electronic device having a processor	Watts, LaVaughn F. JR. et al.
56	US 20010001880 A1	20010524	22	Real-time power conservation and thermal management for electronic devices	Watts, La Vaughn F. JR.
57	US 7229355 B2	20070612	53	External memory system having programmable graphics processor for use in a video game system of the like	San; Jeremy E. et al.
58	US 7134011 B2	20061107	71	Apparatus, architecture, and method for integrated modular server system providing dynamically power-managed and work-load managed network devices	Fung; Henry T.
59	US 7061972 B1	20060613	11	GPS receiver having dynamic correlator allocation between a memory-enhanced channel for acquisition and standard channels for tracking	Best; Gregory C.

	Document ID	Issue Date	Pages	Title	Inventor
60	US 7058826 B2	20060606	71	System, architecture, and method for logical server and other network devices in a dynamically configurable multi-server network environment	Fung; Henry T.
61	US 7032119 B2	20060418	71	Dynamic power and workload management for multi-server system	Fung; Henry T.
62	US 7028198 B2	20060411	15	Processor having real-time power conservation	Watts, Jr.; LaVaughn F. et al.
63	US 6969352 B2	20051129	53	Ultrasound probe with integrated electronics	Chiang; Alice M. et al.
64	US 6901524 B2	20050531	30	Processor having real-time power conservation and thermal management	Watts, Jr.; La Vaughn F.
65	US 6895470 B2	20050517	53	External memory system having programmable graphics processor for use in a video game system or the like	San; Jeremy E. et al.
66	US 6859882 B2	20050222	71	System, method, and architecture for dynamic server power management and dynamic workload management for multi-server environment	Fung; Henry T.
67	US 6848054 B1	20050125	21	Real-time computer thermal management and power conservation	Watts, Jr.; La Vaughn F.
68	US 6795491 B2	20040921	76	Spread spectrum localizers	Fleming; Robert Alan et al.
69	US 6732284 B2	20040504	15	Processor having real-time power conservation	Watts, Jr.; LaVaughn F. et al.
70	US 6732283 B2	20040504	15	Processor having real-time power conservation	Watts, Jr.; LaVaughn F. et al.
71	US 6728248 B1	20040427	38	Optical line terminal	Uchida; Kazuhiro et al.
72	US 6715096 B2	20040330	65	Interface circuit device for performing data sampling at optimum strobe timing by using stored data window information to determine the strobe timing	Kuge; Shigehiro
73	US 6701446 B2	20040302	15	Power control system for synchronous memory device	Tsern; Ely K. et al.
74	US 6646653 B2	20031111	52	Programmable graphics processor for use in a video game system or the like	San; Jeremy E. et al.

	Document ID	Issue Date	Pages	Title	Inventor
75	US 6646514 B2	20031111	7	System and method for reducing startup time in a temperature controlled crystal oscillator	Sutliff; Richard N. et al.
76	US 6633988 B2	20031014	16	Processor having real-time power conservation	Watts, Jr.; LaVaughn F. et al.
77	US 6629256 B1	20030930	14	Apparatus for and method of generating a clock from an available clock of arbitrary frequency	Ilan; Havin et al.
78	US 6442722 B1	20020827	28	Method and apparatus for testing circuits with multiple clocks	Nadeau-Dostie; Benoit et al.
79	US 6427211 B2	20020730	23	Real-time power conservation and thermal management for electronic devices	Watts, Jr.; La Vaughn F.
80	US 6400754 B2	20020604	75	Spread spectrum localizers	Fleming; Robert Alan et al.
81	US 6397340 B2	20020528	17	Real-time power conservation for electronic device having a processor	Watts, Jr.; LaVaughn F. et al.
82	US 6385268 B1	20020507	75	Spread spectrum localizers	Fleming; Robert Alan et al.
83	US 6205524 B1	20010320	13	Multimedia arbiter and method using fixed round-robin slots for real-time agents and a timed priority slot for non-real-time agents	Ng; David Way
84	US 6173409 B1	20010109	17	Real-time power conservation for electronic device having a processor	Watts, Jr.; LaVaughn F. et al.
85	US 6158012 A	20001205	22	Real-time power conservation and thermal management for computers	Watts, Jr.; LaVaughn F.
86	US 6157978 A	20001205	16	Multimedia round-robin arbitration with phantom slots for super-priority real-time agent	Ng; David Way et al.
87	US 6006336 A	19991221	16	Real-time power conservation for computers	Watts, Jr.; LaVaughn F. et al.
88	US 6002708 A	19991214	76	Spread spectrum localizers	Fleming; Robert Alan et al.
89	US 5940771 A	19990817	56	Network supporting roaming, sleeping terminals	Gollnick; Charles D. et al.
90	US 5930516 A	19990727	17	Real time power conservation for computers	Watts, Jr.; LaVaughn F. et al.
91	US 5896561 A	19990420	39	Communication network having a dormant polling protocol	Schrader; Steven B. et al.

	Document ID	Issue Date	Pages	Title	Inventor
92	US 5850230 A	19981215	52	External memory system having programmable graphics processor for use in a video game system or the like	San; Jeremy E. et al.
93	US 5748891 A	19980505	75	Spread spectrum localizers	Fleming; Robert Alan et al.
94	US 5724497 A	19980303	53	Programmable graphics processor having pixel to character conversion hardware for use in a video game system or the like	San; Jeremy E. et al.
95	US 5710910 A	19980120	28	Asynchronous self-tuning clock domains and method for transferring data among domains	Kehl; Theodore H. et al.
96	US 5528621 A	19960618	35	Packet data communication system	Heiman; Fredric et al.
97	US 5388841 A	19950214	55	External memory system having programmable graphics processor for use in a video game system or the like	San; Jeremy E. et al.
98	US 5357604 A	19941018	55	Graphics processor with enhanced memory control circuitry for use in a video game system or the like	San; Jeremy E. et al.
99	US 5218704 A	19930608	17	Real-time power conservation for portable computers	Watts, Jr.; LaVaughn F. et al.
100	US 5172341 A	19921215	10	Serial dram controller with multi generation interface	Amin; Pravin T.
101	US 5033027 A	19910716	10	Serial DRAM controller with multi generation interface	Amin; Pravin T.
102	US 4569584 A	19860211	44	Color electrographic recording apparatus	St. John; Robert P. et al.
103	US 4327294 A	19820427	35	Combined cycle electric power plant and a gas turbine having an improved overspeed protection system	Smith; Jack R. et al.
104	US 4203096 A	19800513	14	Sensor monitoring alarm system	Farley; Max F. et al.
105	US 4195231 A	19800325	80	Combined cycle electric power plant having an improved digital/analog hybrid gas turbine control system	Reed; Terry J. et al.
106	US 4167096 A	19790911	35	Combined cycle electric power plant and a gas turbine having an improved overspeed protection system	Smith; Jack R. et al.

	Document ID	Issue Date	Pages	Title	Inventor
107	US 4151723 A	19790501	19	Refrigeration system control method and apparatus	Gardner; Edward B.
108	US 4151562 A	19790424	25	Methods and apparatus for transmitting image data	Tregay; John L.
109	US 4133275 A	19790109	88	Automatic sewing machine	Herzer; William P. et al.
110	US 4051794 A	19771004	86	Automatic sewing machine	Herzer; William P. et al.
111	US 3974645 A	19760817	46	Control apparatus for matching the exhaust flow of a gas turbine employed in a combined cycle electric power generating plant to the requirements of a steam generator also employed therein	Smith; Jack R.
112	US 3973391 A	19760810	48	Control apparatus for modulating the inlet guide vanes of a gas turbine employed in a combined cycle electric power generating plant as a function of load or inlet blade path temperature	Reed; Terry J. et al.
113	US 3965674 A	19760629	27	Combined cycle electric power plant and a gas turbine having a backup control system with an improved feedforward analog speed/load control	Hobbs; Milton M. et al.
114	US 3909515 A	19750930	22	Facsimile system with memory	Evansen; Arthur W.
115	US 3668653 A	19720606	124	CONTROL SYSTEM	Fair; Donald G. et al.

	Document ID	Issue Date	Pages	Title	Inventor
1	US 20040203834 A1	20041014	112	Remote radio data communication system with data rate switching	Mahany, Ronald L.
2	US 20040073933 A1	20040415	59	Network supporting roaming, sleeping terminals	Gollnick, Charles D. et al.
3	US 20040023651 A1	20040205	59	Network supporting roaming, sleeping terminals	Gollnick, Charles D. et al.
4	US 20030078006 A1	20030424	276	Remote radio data communication system with data rate switching	Mahany, Ronald L.
5	US 20020126737 A1	20020912	10	Background processing and searching for a communication channel	Ofir, Edlis et al.
6	US 20010053174 A1	20011220	82	Spread spectrum localizers	Fleming, Robert Alan et al.
7	US 20010033607 A1	20011025	82	Spread spectrum localizers	Fleming, Robert Alan et al.
8	US 7061972 B1	20060613	11	GPS receiver having dynamic correlator allocation between a memory-enhanced channel for acquisition and standard channels for tracking	Best; Gregory C.
9	US 6795491 B2	20040921	76	Spread spectrum localizers	Fleming; Robert Alan et al.
10	US 6400754 B2	20020604	75	Spread spectrum localizers	Fleming; Robert Alan et al.
11	US 6385268 B1	20020507	75	Spread spectrum localizers	Fleming; Robert Alan et al.
12	US 6002708 A	19991214	76	Spread spectrum localizers	Fleming; Robert Alan et al.
13	US 5940771 A	19990817	56	Network supporting roaming, sleeping terminals	Gollnick; Charles D. et al.
14	US 5896561 A	19990420	39	Communication network having a dormant polling protocol	Schrader; Steven B. et al.
15	US 5748891 A	19980505	75	Spread spectrum localizers	Fleming; Robert Alan et al.

	Document ID	Issue Date	Pages	Title	Inventor
16	US 5528621 A	19960618	35	Packet data communication system	Heiman; Fredric et al.